

JUNCTION FIELD EFFECT TRANSISTOR 2SK508

HIGH FREQUENCY AMPLIFIER N-CHANNEL SILICON JUNCTION FIELD EFFECT TRANSISTOR

<R> DESCRIPTION

The 2SK508 is low input capacitance and High forward transfer admittance, it is suitable for AM tuner, wireless installation and cordless telephone.

<R> FEATURES

• Low input capacitance

$$C_{iss} = 4.8 pF TYP. (V_{DS} = 5.0 V, I_{D} = 10 mA, f = 1.0 MHz)$$

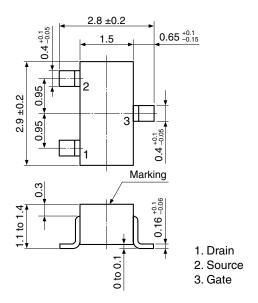
· High forward transfer admittance

 $| y_{fs} | 2 = 26 \text{ mS TYP.} (V_{DS} = 5.0 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ kHz})$

<R> ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK508	SC-59 (Mini Mold)

PACKAGE DRAWING (Unit: mm)



ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Gate to Drain Voltage	Vgdo	–15	V
Gate to Source Voltage	Vgso	-15	V
Drain to Source Voltage (V _{GS} = -4.0 V)	VDSX	15	V
Drain Current (DC)	lσ	50	mA
Gate Current (DC)	lg	5	mA
Total Power Dissipation	PT	200	mW
Junction Temperature	T_j	150	°C
Storage Temperature	Tstg	-55 to +150	°C

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Document No. D18962EJ2V0DS00 (2nd edition) (Previous No. TC-1651)
Date Published December 2007 NS

ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Gate Cut-off Current	Igss	Igss Vgs = -10 V, Vps = 0 V			-1.0	nA
Zero Gate Voltage Drain Current Note	Inss	lpss Vps = 5.0 V, Vgs = 0 V		20	50	mA
Gate to Source Cut-off Voltage	V _{GS(off)}	$V_{DS} = 5.0 \text{ V}, I_{D} = 10 \mu\text{A}$	-0.6	-1.4	-3.5	V
Forward Transfer Admittance Note	y _{fs} 1	V _{DS} = 5.0 V, I _D = 10 mA, f = 1.0 kHz	14	19		mS
	y _{fs} 2	V _{DS} = 5.0 V, V _{GS} = 0 V, f = 1.0 kHz	14	26		mS
Input Capacitance	Ciss	V _{DS} = 5.0 V, I _D = 10 mA, f = 1.0 MHz		4.8		pF
Feedback Capacitance	Crss	V _{DS} = 5.0 V, I _D = 10 mA, f = 1.0 MHz		1.6		pF

 $\textbf{Note} \ \ \text{Pulsed: PW} \leq 1 \ ms, \ \text{Duty Cycle} \leq 1\%$

IDSS CLASSIFICATION

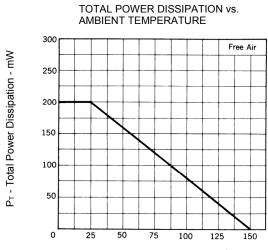
MARKING	K51	K52	K53
Inss (mA)	10 to 20	15 to 30	25 to 50

2

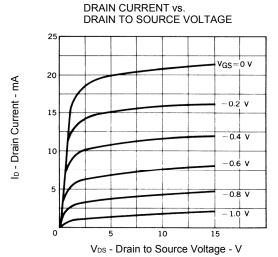
Ib - Drain Current - mA

y_{fs} |1, 2 - Forward Transfer Admittance - mS

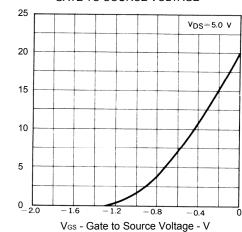
TYPICAL CHARACTERISTICS (TA = 25°C)



T_A - Ambient Temperature - °C

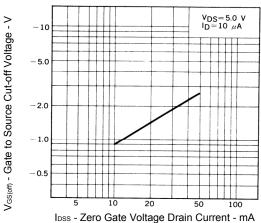


DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE

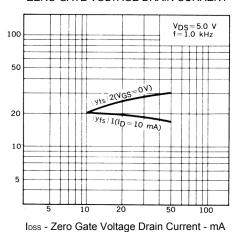


 $V_{DS} = 5.0 \text{ V} \\ I_{D} = 10 \ \mu\text{A}$

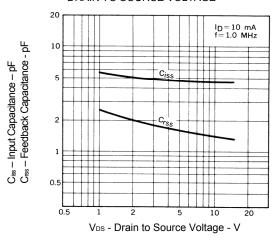
GATE TO SOURCE CUT-OFF VOLTAGE vs. ZERO GATE VOLTAGE DRAIN CURRENT



FORWARD TRANSFER ADMITTANCE vs. ZERO GATE VOLTAGE DRAIN CURRENT



INPUT AND FEEDBACK CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



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